Tutorial 2A: New Approaches to Flash Memory Subsystems

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Agenda

- Extending the NVMHCI Standard to the Enterprise
  - Amber Huffman, Intel
- Improving System Performance and Longevity with a New NAND Flash Architecture
  - Jin-Ki Kim, MOSAID
- 3Bit Per Cell NAND Flash
  - Terry Grunzke, Micron Technology
- Toggle-mode NAND to Fill Growing Need for Higher Performance
  - Harry Yoon, Samsung Semiconductor
- NAND Flash Architecture and Specification Trends
  - Michael Abraham, Micron Technology

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